

International Journal of Engineering Research and Applications (IJERA)

CERTIFICATE

It is certify that the paper entitled by "FPGA Based Power Efficient VLSI Architecture for Phase Measurement" has been published in International Journal of Engineering Research and Applications (IJERA).

Your article has been published with following details:

Author's Name: Nagavalli vegesna,K.Yughandar, Sai Susmitha Kota, Jagadeesh

Koppisetti, Akash kakumani, Madan Sai NiteeshPulletikurthi

Journal Name: International Journal of Engineering Research and Applications (IJERA)

Journal Web: www.ijera.com

Journal Type: Online & Print (Both)

Review Type: Peer Review Refereed

ISSN: 2248-9622

Vol No.: 09

Issue No.: 06

Publication Month: June

Publication Year: 2019



Editor-In-Chief

International Journal of Engineering Research and Applications (IJERA)

E-mail ID: ijera.editor@gmail.com

Web: www.ijera.com